

Notice of References Cited	Application/Control No. 09/505,748	Applicant(s)/Patent Under Reexamination SINGH ET AL.	
	Examiner Kandasamy Thangavelu	Art Unit 2123	Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,477,683	11-2002	Killian et al.	716/1
	B	US-6,349,297	02-2002	Shaw ey al.	707/4
	C	US-6,053,947	04-2000	Parson	703/14
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	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
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NON-PATENT DOCUMENTS

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	U	Calderon et al., "Implementation of a SDH STM-N IC for B-ISDN using VHDL based synthesis tools", ACM, September 1994.
	V	Genoe et al., "On the use of VHDL-based behavioral synthesis for telecom ASIC design", ACM, September 1995.
	W	Silburt et al., "Accelerating concurrent hardware design with behavioral modeling and system simulation", ACM, January 1995.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.